

AMENDMENTS TO THE ABSTRACT:

A ~~one-bus~~ single memory bus multi-media computer system is provided, including a CPU/Sound/Graphic unit, a bus arbitrator, a program and sound and graphic memory for communicating with the CPU/Sound/Graphic unit and the bus arbitrator. Only a single memory bus is required as communication ~~communications~~ is through the bus arbitrator. The addition of the bus-arbitrator can relieve the CPU/Sound/Graphic unit from performing bus synchronization and waiting for the slow memory to catch up.